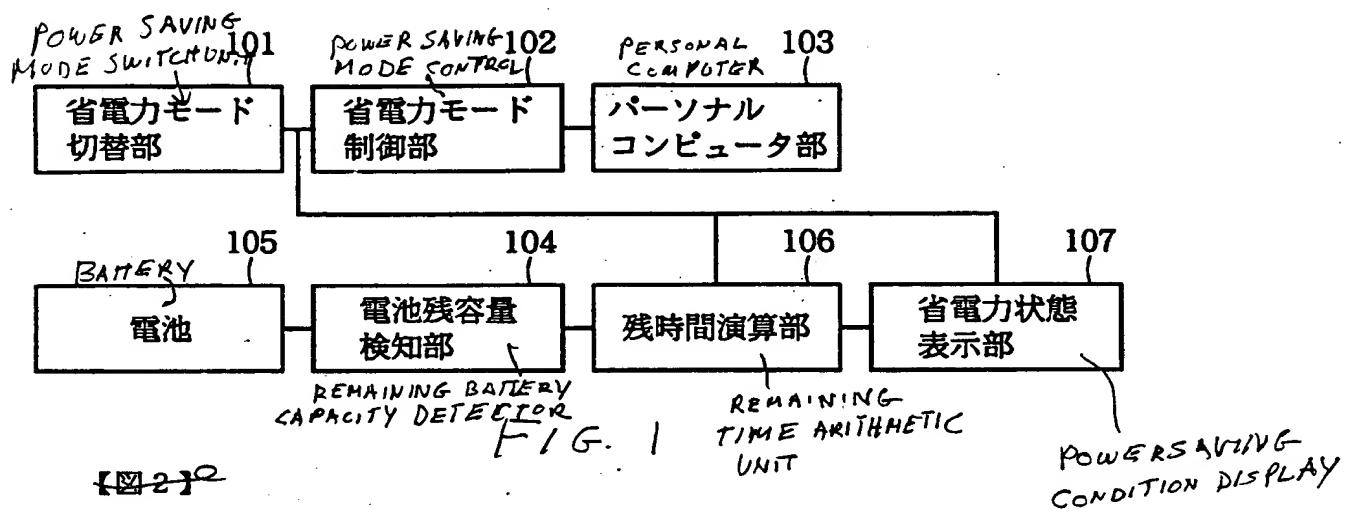


【審査名】

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【図1】



【図2】

FIG. 2

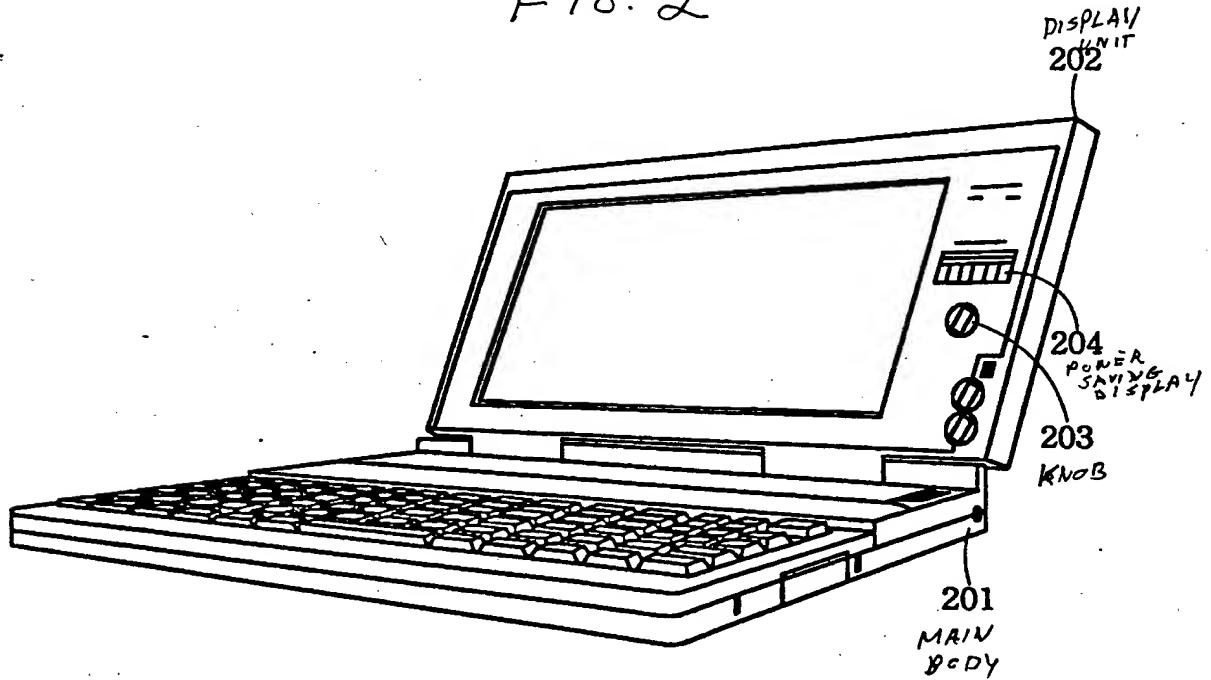


FIG 3P

FIG. 3

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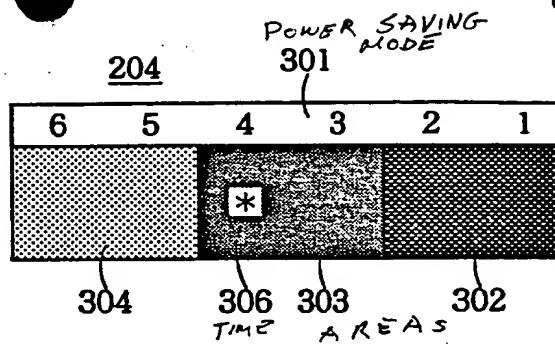
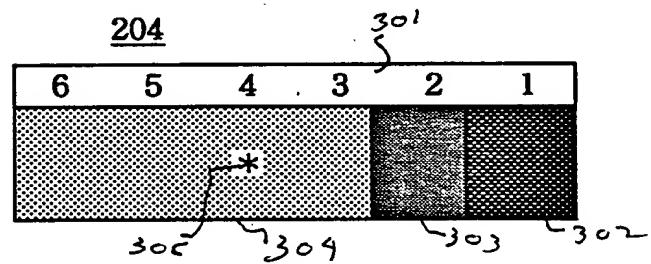


FIG 4

FIG. 4A

(a)



(b)

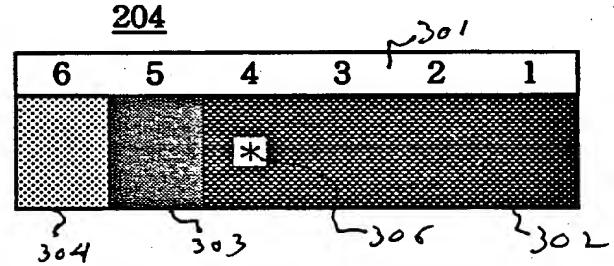


FIG. 4B

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図5

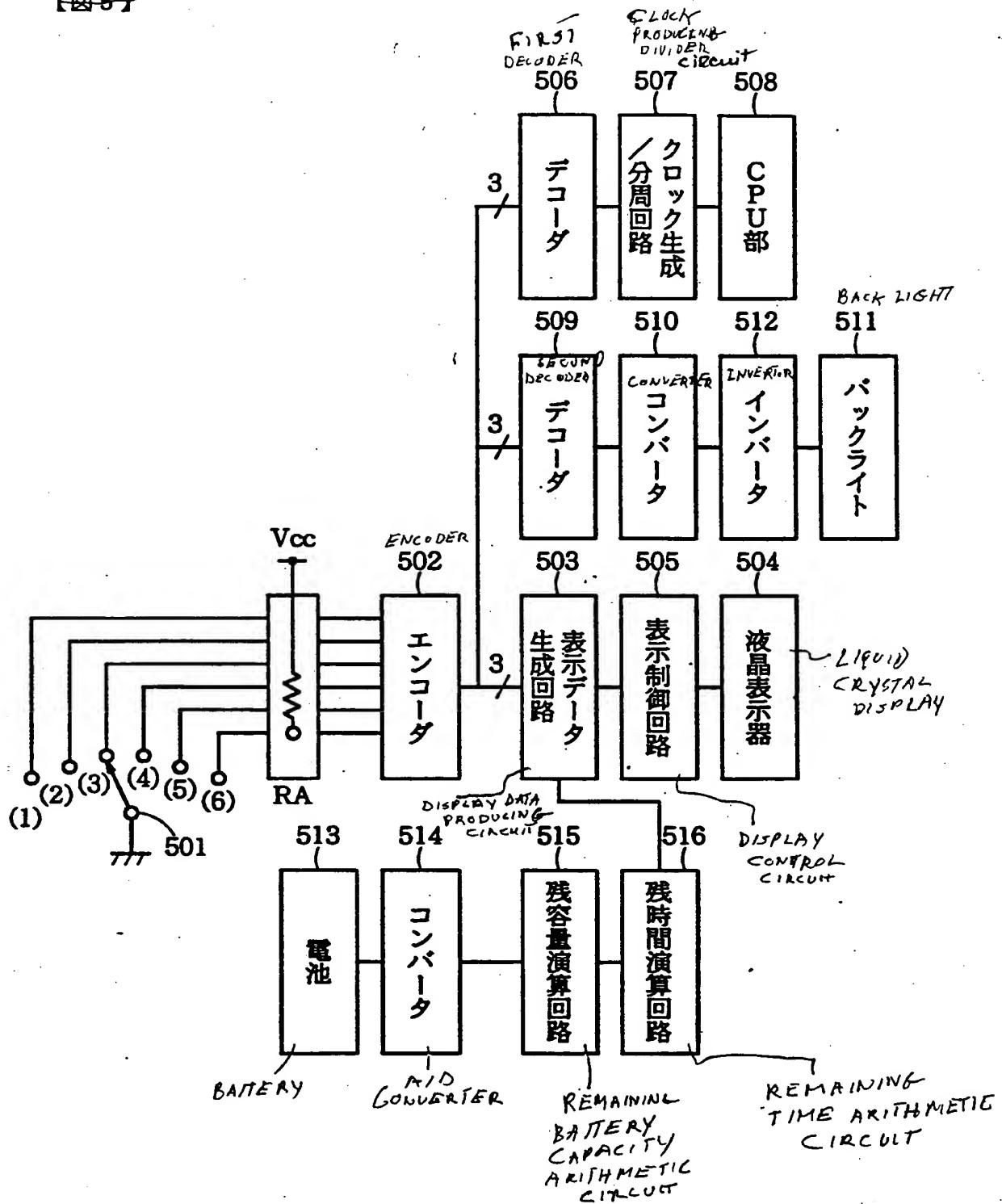


FIG. 5

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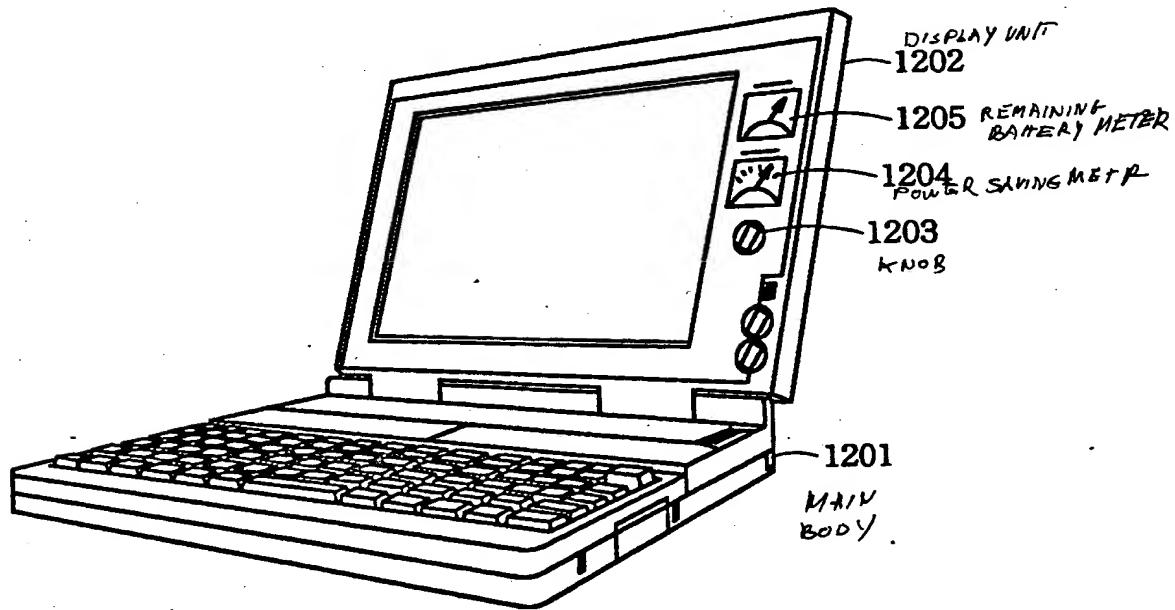
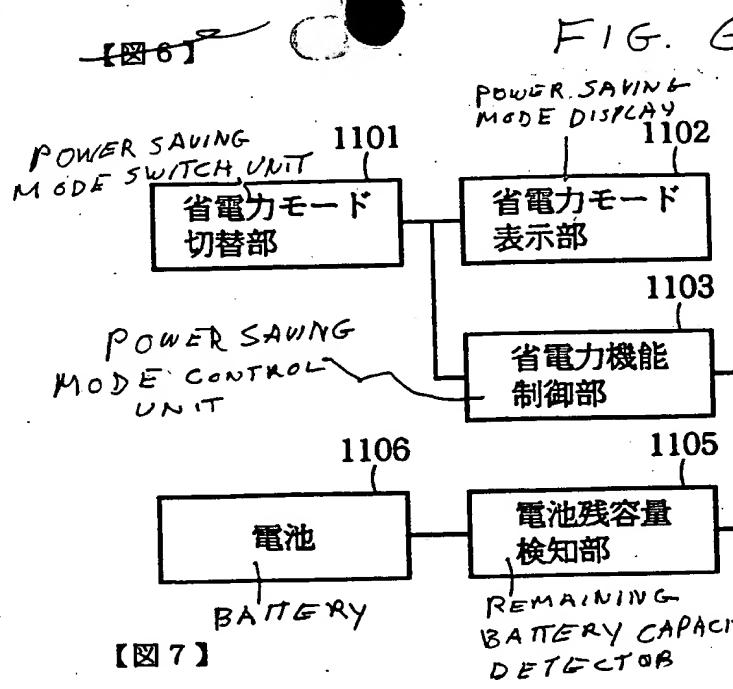
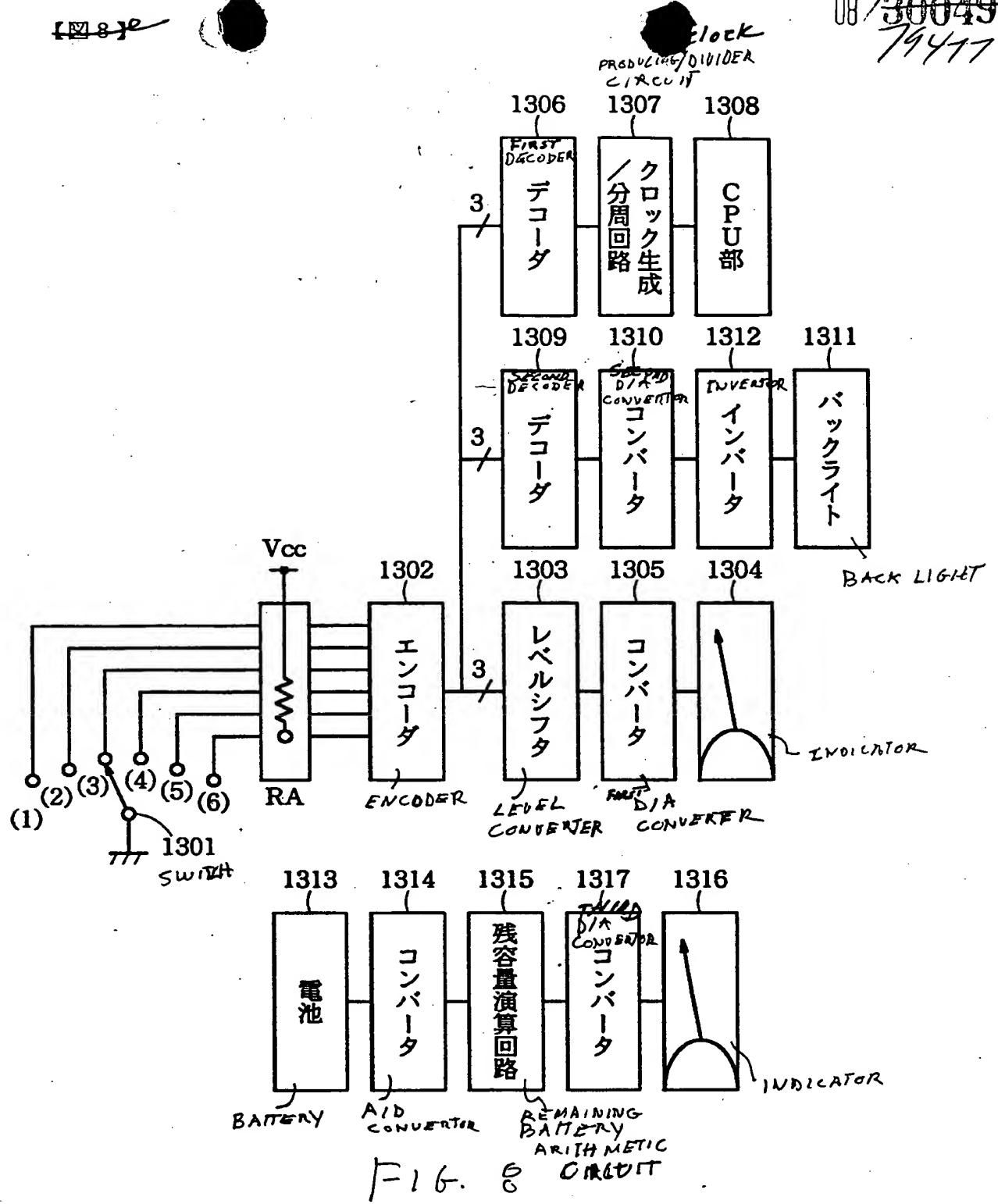


FIG. 7

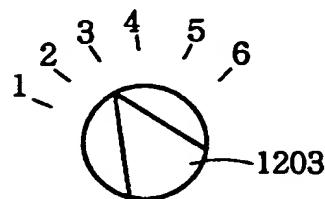
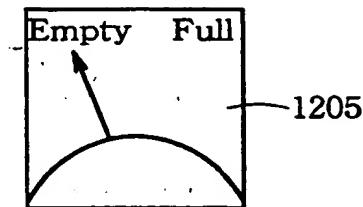
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FIG. 9

Battery Capacity



Power Saving Mode

图5-1 (表示信号生成回路)

FIG 10

